



**Sterne Kessler
Goldstein Fox**
ATTORNEYS AT LAW

Robert George Sterne
Jorge A. Goldstein
David L.S. Cornwell
Robert W. Edmund
Tracy Gene G. Durkin
Michael A. Cimbala
Michael B. Ray
Robert F. Schall
Eric K. Steffe
Michael G. Lee
John M. Couert
Robert C. Jellison
Donald J. Featherstone
Timothy L. Shea, Jr.
Michael V. Macgregor
Judith Li Kim
Marti Fox Evans
Jeffrey T. Healey
Elaine L. Elson
Donald R. Bonowitz
Peter A. Jackman
Bliss L. Del Buono

Elizabeth J. Haines
Michael D. Specht
Erwin W. McCabe
Glenn J. Perry
Theodore A. Wood
Gabe L. Longworth
Edward W. Wee
Grant E. Reed
Jason D. Eisenberg
Rory L. Muller
Jon E. Wright
Ludene M. DeSantis
Helene C. Carlson
Cynthia M. Bouchez
Timothy A. Doyle
Lori A. Gordon
Shannon A. Carroll
Anthe F. Khal
Michelle K. Holubek
Marsha A. Rose
Scott A. Schaller
Liz Zhou

W. Blake Gehlert
James J. Pohl
John J. Haines
Mark W. Rygel
Michael R. Blank
Celia J. Kim Kim
Doyle A. Sover
Lillian Winkler Jenkins
Paul A. Calvo
C. Matthew Rader
Randall K. Baldwin
Lori M. Brando
Jennifer M. Kline
Suzanne L. Elmer
Jeffrey C. Mills
Mia Mushenberg
Scott M. Woodhouse
Peter A. Socatas
Christina A. Carmace
Richard D. Collier
Patricia P. Hansen
Ross G. Hicks

Kelsa Hyton-Rodriguez
Gene A. Lang
Bernie Harfenist-Combs
Alyssa K. Singshwar
Jonathan M. Strong
John P. Vorenski
Chenghua Luo
Sakshar M. Bees
Bruce B. Varne
Justin L. Silver
Byron L. Picard
Kelle K. Dikupst
Richard B. Anton
Christopher B. Ferenc
Jeffrey B. Fogarty
William P. Ladd

Gaurav Asthana
Yasser Moustafa
Cynthia L. Deffenzo
Steve F. Allen
Ralph W. Powers III
Erik C. Wong
Joseph E. Mischelknaus
Kavin Nandakumar
Aaron S. Ward
Of Counsel
Edward J. Kessler
Kenneth C. Bass III
Christopher P. Whit
David C. Isaacson

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*Practice limited to
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WRITER'S DIRECT NUMBER:
(202) 772-8629
INTERNET ADDRESS:
DONT@SKGF.COM

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Art Unit 2822

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Re: U.S. Utility Patent Application
Application No. 10/780,877; Filing Date: February 19, 2004
For: **Overlay Correction By Reducing Wafer Slipping After Alignment**
Inventor: Peter KOCHERSPERGER
Our Ref: 1857.2140000

Sir:

Transmitted herewith for appropriate action is the following document:

1. Amendment and Reply Under 37 C.F.R. §1.116.

The above-listed document is filed electronically through EFS-Web.

In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Donald J. Featherstone
Attorney for Applicant
Registration No. 33,876

DJF/A-S:smn
Enclosures

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